Q 2 (a) Derive the expression for Coulomb's Law of Electrostatics.
Answer Page Number 45 \& 46 of Textbook-I
Q 2 (b) What is meant by Self Induced EMF and derive an expression for the coefficient of self induction.

Answer Page Number 118 \& 119 of Textbook-I

Q 3 (a) State and explain Superposition Theorem.
Answer Page Number 16 \& 17 of Textbook-I

Q 3 (b) Explain the relationship between Line and Phase quantities in Delta connected circuit with the help of a phasor diagram.

Answer Page Number 283 \& 284 of Textbook-I

Q 4 (a) Explain the principle of operation of DC motor with neat sketches.
Answer Page Number 505 \& 506 of Textbook-I

Q 4 (b) A 230 volts dc shunt motor runs at 1000 rpm when the armature current is 35 A . The resistance of the armature circuit is $0.3 \Omega$. Calculate the additional resistance required in the armature circuit to reduce the speed of the motor to 750 rpm, assuming that the armature current is 25 A .

$$
\begin{aligned}
& \text { Answer } \\
& \begin{array}{l}
\text { Voltage applied to the motor } \quad=230 \mathrm{~V} \\
\text { Initial speed, } \mathrm{N}_{1} \\
\text { Armature current at } 1000 \mathrm{rpm} \\
\text { Resistance of the armature circuit, } \mathrm{R}_{\mathrm{a}}=0.3 \Omega \\
\text { Back emf when the motor runs at } 1000 \mathrm{rpm}, \\
\mathrm{E}_{\mathrm{b} 1}=203-35 \mathrm{X} 0.3=219.5 \mathrm{~V}
\end{array}
\end{aligned}
$$

Let the additional resistance in the armature circuit to reduce the speed to 750 rpm be R $\Omega$.
Then the total resistance of the armature circuit $=(0.3+\mathrm{R}) \Omega$
Armature current at $750 \mathrm{rpm}=25 \mathrm{~A}$
Thus, back emf at $750 \mathrm{rpm}, \mathrm{E}_{\mathrm{b} 2} \quad=230-25(0.3+\mathrm{R})$ $=222.5-25 \mathrm{R}$
Shunt field current during the change of speed remains constant, as such the flux at 1000 rpm is equal to the flux at 750 rpm or $\phi_{1}=\phi_{2}$.

Back emf for a particular motor $=\mathrm{K} \phi \mathrm{N}$
Where K is a constant for the motor.
Hence
$\mathrm{E}_{\mathrm{b} 1}=\mathrm{K} \phi_{1} \mathrm{~N}_{1}$
$\mathrm{E}_{\mathrm{b} 2}=\mathrm{K} \phi_{2} \mathrm{~N}_{2}$

Dividing equation (ii) by equation (i), we get

$$
\begin{gathered}
\frac{E_{b 2}}{E_{b 1}}=\frac{\phi_{2}}{\phi_{1}} X \frac{N_{2}}{N_{1}} \\
\frac{222.5-25 R}{219.5}=\frac{\phi_{2}}{\phi_{1}} X \frac{750}{1000} \\
\text { Or } 222500-25000 R=750 \times 219.5
\end{gathered}
$$

Therefore, the additional resistance required in the armature circuit is, $R=2.315 \Omega$
Q 5 (a) What is Step-Up transformer? Derive an expression for the EMF equation a Transformer.

Answer Page Number 402,404 \& 405 of Textbook-I

Q 5 (b) An 8-pole alternator runs at 750 rpm. It supplies power to a 6-pole, 3-phase the induction motor, which has a full load slip of $3 \%$. Find
(i) The speed of the induction motor and
(ii) The frequency of its rotor EMF.

## Answer

Number of poles of the alternator $\mathrm{P}=8$
Speed at which alternator runs $\mathrm{N}=750 \mathrm{rpm}$
Frequency of the alternator, $f=\frac{P N}{120}=\frac{8 X 750}{120}=50 \mathrm{~Hz}$
Synchronous speed of the rotating magnetic field produced in the stator of 3phase
Induction motor is $N_{s}=\frac{120 X f}{P}$
Where f is the frequency of the supply to the induction motor and P the number of poles
of the induction motor is $N_{s}=\frac{120 X 50}{6}=1000 \mathrm{rpm}$
Full load slip, $\mathrm{s}=3 \%=0.03$
Slip of induction motor, $s=\frac{N_{S}-N_{F}}{N_{S}}$
Full load speed of induction motor $N_{F}=N_{S}(1-s)=1000(1-0.03)$

$$
\text { Therefore, } \mathbf{N}_{\mathbf{F}}=\mathbf{9 7 0} \mathbf{~ r p m}
$$

(ii)Frequency of the rotor emf $f_{r}=$ slip $X$ supply frequency

$$
\mathrm{f}_{\mathrm{r}}=0.03 \times 50=1.5 \mathrm{~Hz}
$$

Therefore, the frequency of rotor $\mathbf{e m f}=1.5 \mathrm{~Hz}$

Q 6 (a) What is meant by doping? Explain donor doping with neat diagram.
Answer Page Number $10 \& 11$ of Textbook-II

Q 6 (b) Discuss the typical forward and reverse characteristics of a germanium diode and compare it with silicon diode characteristics.

Answer Page Number 35 \& 36 of Textbook-II
Q 7 (a) Draw the circuit of Full Wave Bridge Rectifier and explain its operation with input and Output waveforms.

Answer Page Number 77 \& 78 of Textbook-II
Q 7 (b) What is a Clamper? Explain the operation of negative voltage clamper circuit with the help of input and output waveforms.

Answer Page Number 121 \& 122 of Textbook-II
Q 8 (a) Draw and explain the Common-Base output characteristics. Label different regions on the characteristics.

Answer Page Number 162, 163 \& 164 of Textbook-II
Q 8 (b) Design a collector-to-base bias circuit to have $V_{C E}=5 \mathrm{~V}$ and $\mathrm{I}_{\mathrm{C}}=5 \mathrm{~mA}$, when the supply voltage is 15 V and the transistor $h_{\mathrm{FE}}$ is 100 .

## Answer

$\mathrm{V}_{\mathrm{CE}}=5 \mathrm{~V} ; \mathrm{I}_{\mathrm{C}}=5 \mathrm{~mA} ; \mathrm{V}_{\mathrm{CC}}=15 \mathrm{~V}$ and $\mathrm{h}_{\mathrm{FE}}=100$
As we know that the relation between the Base Current ( $\mathrm{I}_{\mathrm{B}}$ ), Collector Current ( $\mathrm{I}_{\mathrm{C}}$ ) and current gain ( $\mathrm{h}_{\mathrm{FE}}$ ) as
$I_{B}=\frac{I_{C}}{h_{F E}}=\frac{5 m A}{100}=50 \mu \mathrm{~A}$

From the bias cicuit shown resistance $\mathrm{R}_{\mathrm{C}}$ Is given by
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Collector-to-Base in Fig.1, the collector

$$
R_{C}=\frac{V_{C C}-V_{C E}}{I_{C}+I_{B}}=\frac{15 V-5 V}{5 m A+50 \mu \mathrm{~S}}=1.98 \mathrm{~K} \Omega .
$$

From the figure, the base resistance $\left(\mathrm{R}_{\mathrm{B}}\right)$ is given by

$$
R_{B}=\frac{V_{C E}-V_{B E}}{I_{B}}=\frac{5 V-0.7 V}{50 \mu \mathrm{~A}}=86 \mathrm{~K} \Omega .
$$

The designed Collector-to-Base Bias Circuit is shown in Fig. 2


Fig. 2 Collector-to-Base Bias Circuit

Q 9 (a) Draw the circuit of series voltage negative feedback and derive an expression for its voltage gain .

Answer Page Number 545 to 547 of Textbook-II
Q 9 (b) What is an Oscillator? Explain the operation of BJT Hartley oscillator.
Answer Page Number 665, 666, 675 \& 676 of Textbook-II

## Text Books

1. V.N. Mittle and Arvind Mittal, 'Basic Electrical Engineering', Tata McGraw-Hill Publishing Company Limited, 2nd edition, 2006.
2. Electronic Devices and Circuits, David A Bell, Fourth Edition, PHI (2006).
