ROLL NO.	

Subject: ADVANCED MICROPROCESSORS Code: AC78/AC133

AMIETE - CS (Current & New Scheme)

JUNE 2017 Time: 3 Hours Max. Marks: 100

PLEASE WRITE YOUR ROLL NO. AT THE SPACE PROVIDED ON EACH PAGE IMMEDIATELY AFTER RECEIVING THE QUESTION PAPER.

NOTE: There are 9 Questions in all.

- Ouestion 1 is compulsory and carries 20 marks. Answer to 0.1 must be written in the space provided for it in the answer book supplied and nowhere else.
- The answer sheet for the Q.1 will be collected by the invigilator after 45 minutes of the commencement of the examination.
- Out of the remaining EIGHT Questions answer any FIVE Questions. Each question

Any required data not explicitly given, may be suitably assumed and stated.					
Q.1	Choose the correct or the best alternative in the following: (2:				
	a.	Which 8086 μP interrupt is having to (A) Single Step Interrupt (B) INTR (C) Non Maskable Interrupt (NMI) (D) Divide by Zero	he highest priority		
	b.	The BIOS services are stored in			
		(A) ROM	(B) SRAM		
		(C) DRAM	(D) Flash Memory		
	c.	c. A .COM program can have maximum size of			
		(A) 8 K Bytes	(B) 16 K Bytes		
		(C) 32 K Bytes	(D) 64 K Bytes		
	d.	In the minimum mode operation of 8 connected to	8086 microprocessor, the pin MN / M	X is	
		(A) Logic 0	(B) Logic1		
		(C) GND	(D) High Impedance		
	e.	The Pentium Processor is based on			
		(A) CISC	(B) DOS		
		(C) EISA	(D) RISC		
	f.	8087 works on bit internally,	1 7		
		(A) 80	(B) 40		
		(C) 60	(D) None of these		
	φ.	Intel 8086 microprocessor operates a	at a frequency of		

(A) 1MHz and 50% duty cycle

(C) 3MHz and 25% duty cycle

(B) 10MHz and 33% duty cycle

(D) 10MHz and 66% duty cycle

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	h.		lag is used with Instruction Instruction		tack Instruction			
	i.	The 8284 CLK generator also generates (A) ALE (B) Test (C) Ready (D) None of these						
	j.	Which type (A) short (C) near	e of JMP instruction	(B) fa		is 0020 h b	ytes?	
		Ansv	wer any FIVE Ques Each questio		_	estions.		_
Q.2	a.	Explain the architecture and various flags of 8086.					(10)	
	b.	What are the different addressing modes supported by 8086? Explain each of them with suitable examples. (6					of (6)	
Q.3	a.	Explain the working of WAIT and LOCK instruction in 8086.				(8)		
	b.	Explain A	AA and DAA instruc	tions with e	xample.			(4)
	c.	Describe th	ne working of MOVS	S and CMPS	S instructions.			(4)
Q.4	a.	10101011, and CL= 03, i.e., 00000011 and initial flag value stored at Carry Flag				ag		
		S.No	all instructions. Instruction	Before Execution		After Execution		(8)
		5.110	mstruction	AL	CF	AL	CF	
		1	SHL AL, CL			112	01	
		2	SAL AL, CL					
		3	SHR AL, CL					
		4	SAR AL, CL					
	b.		rupted by an externa s-8086? Explain. Wh	-	1 1		ram executi	on (8)
Q.5	a.	Explain the	e 8087 instructions to	o load specia	al constants.			(8)
	b.	Explain the	e control register and	status regis	ster of 8087.			(8)
Q.6	a.	Write an as	ssembly language pr comments.	ogram to ge	enerate Fibon	acci series	and also wr	rite (8)

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	b.	Discuss the following assembler directives with example: (i) DWORD (ii) OFFSET (iii) SEGMENT (iv) MACRO	(2×4)
Q.7	a.	Explain the various methods of accessing IBM PC hardware.	(4)
	b.	What are assembler directives? Explain various PTR directives used in 8086.	(4)
	c.	Write an assembly language program to search a key in an array of element using LINEAR SEARCH method.	ts (8)
Q.8	a.	Using BIOS routines, write a C program to display a suitable message on CRT in the middle of the screen, after clearing the screen first.	n (8)
	b.	Mention any four 8087 co-processor instruction. Give an illustration to comput square root.	e (4+4)
Q.9	a.	Mention the various addressing modes available in 80386 microprocessor.	(8)
	b.	Write a C program to read a string from keyboard with DOS interrupt and print the same on the printer, if it is on line. Display a suitable message on the screen if the printer is off.	