**Code: AE68/AE117** 

Subject: EMBEDDED SYSTEMS DESIGN

ROLL NO.

## AMIETE – ET (Current & New Scheme)

**Time: 3 Hours** 

## **June 2018**

Max. Marks: 100

PLEASE WRITE YOUR ROLL NO. AT THE SPACE PROVIDED ON EACH PAGE IMMEDIATELY AFTER RECEIVING THE QUESTION PAPER.

NOTE: There are 9 Ouestions in all.

- Question 1 is compulsory and carries 20 marks. Answer to Q.1 must be written in the space provided for it in the answer book supplied and nowhere else.
- The answer sheet for the Q.1 will be collected by the invigilator after 45 minutes of the commencement of the examination.
- Out of the remaining EIGHT Questions answer any FIVE Questions. Each question carries 16 marks.
- Any required data not explicitly given, may be suitably assumed and stated.

Q.1	<b>Choose the correct or the best alternative in the following</b> a. Which is the primary memory?		(2×10)	
	(A) RAM (C) CD-ROM	( <b>B</b> ) Hard Disk ( <b>D</b> ) Floppy disk		
	<ul> <li>b. Which is the secondary memory?</li> <li>(A) ROM</li> <li>(C) Optical Memory</li> </ul>	<ul><li>(B) RAM</li><li>(D) None of these</li></ul>		
	<ul> <li>c. Which is output unit of the embedde</li> <li>(A) Touch screen</li> <li>(C) Mice</li> </ul>	ed system? (B) Keyboard (D) Digitizer		
	<ul> <li>d. Which is input unit of the embedded</li> <li>(A) LCD Screen</li> <li>(C) Printer</li> </ul>	l system? (B) Video monitor (D) Scanner		
	<ul><li>e. Which is non-networking unit of the (A) Ethernet card</li><li>(C) Bus driver</li></ul>	<ul> <li>embedded system?</li> <li>(B) Front processor based server</li> <li>(D) Semiconductor memories</li> </ul>		
	<ul> <li>f. RTOS is known as</li> <li>(A) Real time open system</li> <li>(C) Real time operating system</li> </ul>	<ul><li>(B) Real time optical system</li><li>(D) None of these</li></ul>		
	<ul> <li>g. ASIC is known as</li> <li>(A) American Standard Integrated Circuit</li> <li>(B) African Standard Integrated Circuit</li> <li>(C) Application Specific Internal Circuit</li> <li>(D) Application Specific Integrated Circuit</li> </ul>			
	<ul> <li>h. ASSP is known as</li> <li>(A) Application Specific System Processor</li> <li>(B) Application Specific synchronous processor</li> <li>(C) American Standard Specific Processor</li> <li>(D) None of these</li> </ul>			
	<ul> <li>i. In current market embedded chip pov</li> <li>(A) increase in area of chip</li> <li>(C) increase in current</li> </ul>	<ul><li>ver consumptions are decreasing due to</li><li>(B) decreases area of chip</li><li>(D) None of these</li></ul>		

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ROLL NO.

- j. Fetch instructions can be considered as:
  - (A) The task of reading previous instruction
  - (B) The task of reading next instruction
  - (C) The task of writing a register
  - **(D)** None of above

	Answer any FIVE Questions out of EIGHT Questions. Each question carries 16 marks.	
Q.2	a. Explain the importance of embedded system.	(3)
	b. Write the common characteristics of Embedded System; Give the five examp of embedded system.	les (5)
	c. Show the difference between FPGA & CPLD. And also show the difference between microcontroller and microprocessor.	(8)
Q.3	a. Differentiate between state encoding and state minimization.	(3)
	b. Draw the circuit diagram of three input CMOS NAND Gate with suitable asp ratio; show the switching action of all the transistors.	ect (5)
	c. What is RTOS Semaphores? Explain its sequence.	(8)
Q.4	a. How memory space saving is important in embedded system? What are the wa to save code space?	ays (8)
	b. What is memory Hierarchy? Draw and explain architecture of basic DRAM.	(8)
Q.5	a. Identify the difference between DSP and microcontroller.	(3)
	b. Explain counter, reaction timer, and watchdog timer. Show the difference betwe counter and timer.	en (5)
	c. How memory is composed. Show with example for composing 4 <sup>K</sup> x16 memory from 2 <sup>K</sup> x8 available memory.	ory (8)
Q.6	a. What is strobed Hand shake protocol?	(3)
	b. Show the importance of embedded system in advanced Wireless communication system. Explain the OSI model to explain the essential protocols.	
	c. What is Scheduler? Show with example.	(8)
Q.7	a. What are pointers, queus, and mail boxes?	(3)
	b. What are the rules for Interrupt routines in an RTOS environment?	(5)
	c. Show the role play of ATM. Explain the controlling process of stepper mo with suitable diagram.	tor ( <b>8</b> )
Q.8	a. What is task structure?	(3)
	b. Demonstrate the machine interfacing. Show the standard structure & protocol advance communications.	for (5)
	c. Define the theme of serial protocols, parallel protocols, and wireless protocols, a HTTP.	und (8)
Q.9	a. Define the Real time operating systems.	(3)
	b. Explain the difference between computer and embedded systems with suita examples.	ble (5)
	c. Demonstrate the roles of real time embedded system in current market.	(8)