Subject: POWER ELECTRONICS

## **DiplETE – ET (Current & New Scheme)**

**Time: 3 Hours** 

DECEMBER 2015

Max. Marks: 100

PLEASE WRITE YOUR ROLL NO. AT THE SPACE PROVIDED ON EACH PAGE IMMEDIATELY AFTER RECEIVING THE QUESTION PAPER.

NOTE: There are 9 Questions in all.

- Question 1 is compulsory and carries 20 marks. Answer to Q.1 must be written in the space provided for it in the answer book supplied and nowhere else.
- The answer sheet for the Q.1 will be collected by the invigilator after 45 minutes of the commencement of the examination.
- Out of the remaining EIGHT Questions answer any FIVE Questions. Each question carries 16 marks.
- Any required data not explicitly given, may be suitably assumed and stated.

Q.1	Choose the correct or the best alternative in the following:	(2×10)
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- a. An AC Voltage Controller is used for \_\_\_\_\_
  - (A) high voltage DC transmission(B) speed control of induction motors
  - (C) induction heating
  - (D) speed control of DC motors
- b. When a power diode is conducting, the voltage drop across it is about\_\_\_\_\_\_
  (A) 1V
  (B) 5V
  (C) 20V
  (D) 50V
- c. An UJT exhibits negative resistance characteristics when its\_\_\_\_\_
  - (A) current is less than valley current
  - (B) current is less than peak current
  - (C) current is more than peak current but less than valley current
  - (D) none of these
- d. The minimum anode current that must flow through the SCR in order for it to stay on after the gate signal is removed is called\_\_\_\_\_
  - (A) Holding current
  - (C) Latching current

(B) Surge current

(D) Repetitive current

- e. During forward blocking state of SCR, the voltage and current respectively are\_\_\_\_\_\_
  (A) high and high
  (B) low and low
  (C) high and low
  (D) low and high
- f. A single phase dual converter consists of \_\_\_\_\_

(C) 9 times the line frequency

- (A) two fully controlled converters connected in parallel
- (B) two fully controlled converters connected in antiparallel
- (C) two fully controlled semiconverters connected in antiparallel
- (D) two fully controlled semiconverters connected in parallel
- g. In a 3 phase fully controlled converter, the firing frequency is \_\_\_\_
  - (A) 3 times the line frequency (B) 6 times the line frequency

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(D) 12 times the line frequency

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	<ul> <li>h. A DC Chopper converts</li> <li>(A) constant voltage dc into ac and then into variable voltage dc</li> <li>(B) constant voltage dc into variable voltage dc directly</li> <li>(C) ac of one frequency into ac of another frequency</li> <li>(D) ac to dc</li> </ul>					
	i.	Inverters finds application in (A) HVDC transmission (C) variable speed ac drives	(B) UPS (D) all of these			
	j.	A cycloconverter is a (A) ac to dc converter (C) dc to dc converter	<ul><li>(B) dc to ac converter</li><li>(D) ac to ac converter</li></ul>			
Answer any FIVE Questions out of EIGHT Questions. Each question carries 16 marks.						
Q.2	a.	What is power loss in an ideal swite junction transistor with the help of cir	ch? Explain the conduction losses in a cuit diagram.	bipolar <b>(8)</b>		
	b.	Explain, how the power diode must be (i) Overvoltage (ii) Overcurrent (iii) Transients	e protected against the following:-	(8)		
Q.3	a.	How does a Power Bipolar Junction characteristics and explain the signific	n Transistor used as a switch? Draw cance of saturation, active and cut-off r	its V-I egions. <b>(8)</b>		
	b.	A Power MOSFET has $I_{DSS} = 2 \text{ mA}$ , $R_{DS(C)}$ 100 V, $t_r = 100 \text{ ns and } t_f = 200 \text{ ns. If th}$ (i) on-state loss (ii) off-state loss (iii) turn-on switching loss (iv) turn-off switching loss	$\sigma_{\rm NN}$ = 0.3 $\Omega$ , duty cycle d = 50%, I <sub>D</sub> = 6 A me frequency of switching is 40 KHz, then	A, V <sub>DS</sub> = n find <b>(8)</b>		
Q.4	a.	What is the necessity of connectin connecting two SCRs in parallel and characteristics.	t is the necessity of connecting two SCRs in parallel? Draw the circuit of necting two SCRs in parallel and explain its operation with the help of on-state acteristics. (9)			
	b.	What are the most common meth commutation method by external sou waveforms.	ods of achieving commutation? Explaurce and explain its operation with the	ain the help of <b>(7)</b>		
Q.5	a.	Draw a neat diagram for Single Phas resistive load and explain its operation	e Full Wave Controlled Bridge Rectifier with the help of waveforms.	with a <b>(10)</b>		
	b. A single phase half wave controlled rectifier connected to a 150 V, 60 Hz source to supplying a resistive load of 10 $\Omega$ . If the delay angle $lpha$ is $30^\circ$ , then find:					
		<ul><li>(i) the maximum load current</li><li>(ii) the average load current</li></ul>		(6)		
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resistive load and explain its operation with waveforms. b. A six pulse half controlled bridge rectifier is connected to a three phase 220 V AC source. Calculate the firing angle if the terminal voltage of the rectifier is 240 V. What is the maximum value of the DC output voltage?

- Q.7 a. What is a DC Chopper? Explain its principle with the help of suitable diagram and waveforms. What are its various industrial applications? (8)
  - b. What is a Buck-Boost Chopper? Draw its circuit configuration and explain its working with the help of voltage and current waveforms. (8)
- a. Explain the working of a full bridge VSI with a neat circuit diagram and waveforms. Q.8

(8)

(10)

(6)

- b. What are the most commonly used methods of pulse width modulation? Explain multiple pulse width modulation with the help of waveforms when the number of pulses (m) = 2 and 3. (8)
- a. What do you mean by AC power control? Discuss the differences between integral Q.9 cycle control and AC phase control. (8)
  - b. Compare the advantages and disadvantages of semiconductor switches over mechanical switches. (8)

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Q.6

### a. Draw a neat diagram for Three Phase Half Wave Controlled Rectifier circuit with a

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