DC61 / DEC. - 2011

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## Diplete – CS (NEW SCHEME) – Code: DC61

## Subject: OPERATING SYSTEMS & SYSTEMS SOFTWARE

**Time: 3 Hours** 

Q.1

## DECEMBER 2011

Max. Marks: 100

ROLL NO.

NOTE: There are 9 Questions in all.

- Please write your Roll No. at the space provided on each page immediately after receiving the Question Paper.
- Ouestion 1 is compulsory and carries 20 marks. Answer to 0.1 must be written in the space provided for it in the answer book supplied and nowhere else.
- The answer sheet for the Q.1 will be collected by the invigilator after 45 Minutes of the commencement of the examination.
- Out of the remaining EIGHT Questions, answer any FIVE Questions, selecting at least TWO questions from each Part. Each question carries 16 marks.
- Any required data not explicitly given, may be suitably assumed and stated.

## Choose the correct or the best alternative in the following: a. .... is the technique of temporarily removing inactive programs from the memory of a computer system. (A) Swapping **(B)** Segmentation (C) Paging (D) Demand paging b. PCB stands for ..... (A) Printed Circuit Board **(B)** Process Control Block (C) Program Controlling Block (**D**) None of the above c. Which of the following is most general phase structured grammar? (A) Context – Sensitive (**B**) Regular (C) Context-Free (D) None of the above d. ..... Language translator convert source program into object program or into machine language by taking one by one instruction. (A) Compiler (**B**) Interpreter (C) Assembler (D) Language translator e. A parser builds a parser tree by starting at the leaves and working up towards the root. (A) Bottom-up

(**B**) Top-down (C) Recursive Descent (D) None of these  $(2 \times 10)$ 

	f.	This refers to the delay between the read/write request, and the appearance of the required sector under the read/write head.		
		<ul><li>(A) Access Time</li><li>(C) Latency Time</li></ul>	<ul><li>(B) Seek Time</li><li>(D) None</li></ul>	
	g.	ry is		
	<ul> <li>(A) confusion of access methods</li> <li>(B) confusion of files data</li> <li>(C) confusion of file names between different users</li> <li>(D) none of the above</li> </ul>			
	h.	h is called a light weight processes		
		<ul><li>(A) Frames</li><li>(B) Threads</li></ul>	<ul><li>(B) Pages</li><li>(D) None of the above</li></ul>	
	i is a binding performed before the execution of a program			
		<ul><li>(A) Dynamic binding</li><li>(C) Code binding</li></ul>	<ul><li>(B) Object binding</li><li>(D) Static binding</li></ul>	
	j.	In r	to search operations are conducted on them	
		<ul><li>(A) Search Data structure</li><li>(C) Non-linear Data structure</li></ul>	<ul><li>(B) Linear Data structure</li><li>(D) Allocation Data structure</li></ul>	
PART A Answer at least TWO questions. Each question carries 16 marks.				
Q.2		Explain about the following in brief	:	
		<ul> <li>(i) Batch operating systems</li> <li>(ii) Process Control Block (PCB)</li> <li>(iii) Multi programming</li> <li>(iv) Time sharing</li> </ul>	( <b>4</b> × <b>4</b> )	
Q.3	a.	What is the difference between pre-	emptive and non-preemptive scheduling?(8)	

- b. State and explain the necessary conditions for deadlock to occur. (4)
- c. Summarise the features of the multiprogramming scheduler. (4)
- Q.4 a. Define critical section problem. Discuss about three requirements that a solution to critical-section problem must satisfy.(8)
  - b. Discuss various allocation methods used in the file system. (8)

Q.5	a.	Differentiate between			
		(i) Paging and segmentation			
		(ii) Internal and External Fragmentation. (8)			
	b.	Consider the following reference string 5, 4, 3, 2, 1, 4, 3, 5, 4, 3, 2, 1, 5			
		Show the behaviour of FIFO page replacement policy considering			
		(i) 3 page frame (alloc = 3)			
		(ii) 4 page frame (alloc = 4)	(8)		
PART B Answer at least TWO questions. Each question carries 16 marks.					
Q.6	a.	a. What do you mean by Intermediate Representation (IR)? What are the desiral properties of an IR? (4)			
	b.	Differentiate between program translation and program interpretation in Give appropriate schematic to explain.	model. (6)		
	c.	What is language processor? Define briefly various categories of lar processor.	nguage (6)		
Q.7	a.	Write a note on LL(1) parser. Make a parser table for an LL(1) parser for the following grammar: E ::= TE' $E' ::= + TE' \in T$ T ::= VT' T' = TT'			
		$1 ::= * \vee 1   \in$ V ::= <id></id>	(10)		
			(10)		
	b.	Explain the similarities and differences between the use of macros and the subroutines.	use of (6)		
Q.8	a.	What are advantages of assembly language?	(4)		
	b.	Discuss various categories of assembly language statement. Give example each category.	ples in (6)		
	c.	What are the tasks performed by synthesis phase of an assembler. List the	ese.(6)		
Q.9	а.	What do you understand by the parameter passing mechanism? Give brief description about			
		<ul><li>(i) Call by value</li><li>(ii) Call by value-result</li><li>(iii) Call by reference</li></ul>	(8)		
	b.	<ul><li>Differentiate between</li><li>(i) Compiler and interpreter</li><li>(ii) Pure and impure interpreter.</li></ul>	(8)		

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